A colossal breakthrough for topological spintronics
BiSb expands the potential of topological insulators for ultra-low-power electronic devices

(Tokyo, July 31) Scientists have developed the world’s best-performing pure spin current source\(^1\) made of bismuth–antimony (BiSb) alloys, which they report as the best candidate for the first industrial application of topological insulators\(^2\). The achievement represents a big step forward in the development of spin-orbit torque magnetoresistive random-access memory (SOT-MRAM)\(^3\) devices with the potential to replace existing memory technologies.

A research team led by Pham Nam Hai at the Department of Electrical and Electronic Engineering, Tokyo Institute of Technology (Tokyo Tech), has developed thin films of BiSb for a topological insulator that simultaneously achieves a colossal spin Hall effect\(^4\) and high electrical conductivity.

Their study, published in *Nature Materials*, could accelerate the development of high-density, ultra-low power, and ultra-fast non-volatile memories for Internet of Things (IoT) and other applications now becoming increasingly in demand for industrial and home use.

The BiSb thin films achieve a colossal spin Hall angle of approximately 52, conductivity of $2.5 \times 10^5$ and spin Hall conductivity of $1.3 \times 10^7$ at room temperature. (See Table 1 for a performance summary, including all units.) Notably, the spin Hall conductivity is two orders of magnitude greater than that of bismuth selenide (Bi\(_2\)Se\(_3\)), reported in *Nature* in 2014.

Making SOT-MRAM a viable choice
Until now, the search for suitable spin Hall materials for next-generation SOT-MRAM devices has been faced with a dilemma: First, heavy metals such as platinum, tantalum and tungsten have high electrical conductivity but a small spin Hall effect. Second, topological insulators investigated to date have a large spin Hall effect but low electrical conductivity.

The BiSb thin films satisfy both requirements at room temperature. This raises the real possibility that BiSb-based SOT-MRAM could outperform the existing spin-transfer torque (STT) MRAM technology.

“As SOT-MRAM can be switched one order of magnitude faster than STT-MRAM, the switching energy can be reduced by at least two orders of magnitude,” says Pham.
“Also, the writing speed could be increased 20 times and the bit density increased by a factor of ten.”

The viability of such energy-efficient SOT-MRAMs has recently been demonstrated in experiments, albeit using heavy metals, conducted by IMEC, the international R&D and innovation hub headquartered in Leuven, Belgium.

If scaled up successfully, BiSb-based SOT-MRAM could drastically improve upon its heavy metal-based counterparts and even become competitive with dynamic random access memory (DRAM), the dominant technology of today.

An attractive, overlooked material
BiSb has tended to be overlooked by the research community due to its small band gap and complex surface states. However, Pham says: “From an electrical engineering perspective, BiSb is very attractive due to its high carrier mobility, which makes it easier to drive a current within the material.”

“We knew that BiSb has many topological surface states, meaning we could expect a much stronger spin Hall effect. That’s why we started studying this material about two years ago.”

The thin films were grown using a high-precision method called molecular beam epitaxy (MBE). The researchers discovered a particular surface orientation named BiSb(012), which is thought to be a key factor behind the large spin Hall effect. Pham points out that the number of Dirac cones on the BiSb(012) surface is another important factor, which his team is now investigating.

Challenges ahead
Pham is currently collaborating with industry to test and scale up BiSb-based SOT-MRAM.

“The first step is to demonstrate manufacturability,” he says. “We aim to show it’s still possible to achieve a strong spin Hall effect, even when BiSb thin films are fabricated using industry-friendly technologies such as the sputtering method.”

“It’s been over ten years since the emergence of topological insulators, but it was not clear whether those materials could be used in realistic devices at room temperature. Our research brings topological insulators to a new level, where they hold great promise for ultra-low power SOT-MRAM.”

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### Table 1. Performance comparison between several heavy metals and topological insulators at room temperature.

<table>
<thead>
<tr>
<th></th>
<th>$\theta_{SH}$</th>
<th>$\sigma$ (Ω$^{-1}$m$^{-1}$)</th>
<th>$\sigma_{SH}$ ($\frac{h}{2e}$Ω$^{-1}$m$^{-1}$)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ta</td>
<td>0.15</td>
<td>5.3×10$^5$</td>
<td>0.8×10$^5$</td>
</tr>
<tr>
<td>W</td>
<td>0.4</td>
<td>4.7×10$^5$</td>
<td>1.9×10$^5$</td>
</tr>
<tr>
<td>Pt</td>
<td>0.08</td>
<td>4.2×10$^6$</td>
<td>3.4×10$^5$</td>
</tr>
<tr>
<td>Bi$_2$Se$_3$</td>
<td>2-3.5</td>
<td>5.7×10$^4$</td>
<td>1.1-2.0×10$^5$</td>
</tr>
<tr>
<td>Bi$<em>x$Se$</em>{1-x}$</td>
<td>18.8</td>
<td>7.8×10$^3$</td>
<td>1.47×10$^5$</td>
</tr>
<tr>
<td>Bi$<em>{0.05}$Sb$</em>{0.1}$ (this work)</td>
<td>52</td>
<td>2.5×10$^5$</td>
<td>1.3×10$^7$</td>
</tr>
</tbody>
</table>

$\theta_{SH}$: spin Hall angle, $\sigma$: conductivity, $\sigma_{SH}$: spin Hall conductivity.

The figures in the bottom row are those achieved in the present study. Remarkably, the spin Hall conductivity, shown in the right-hand column, is two orders of magnitude greater than the previous record.

#### Technical terms

1. **Pure spin current**: A phenomenon whereby spin angular momentum, but not charge, is transported.

2. **Topological insulators**: Materials with highly electrically conductive surfaces, but which act as insulators on the inside. Such materials are of great interest in the quest to develop high-performing electronic devices that generate less heat.

3. **SOT-MRAM**: Spin-orbit torque (SOT) switching using the spin Hall effect is increasingly viewed as a successor to conventional spin-transfer torque (STT) switching for magnetoresistive random-access memory (MRAM) technologies.

4. **Spin Hall effect**: A Hall effect for spins originating from the coupling of charge and spin. Pure spin currents can be generated via the spin Hall effect.

5. **Band gap**: An energy range in an insulator or semiconductor in which no electron states can exist.

6. **Dirac cones**: Unique electronic structures that occur in topological insulators that represent linear energy dispersion.

#### References
Nguyen Huynh Duy Khang¹, Yugo Ueda¹, and Pham Nam Hai¹-²* A conductive
topological insulator with large spin Hall effect for ultra-low power spin-orbit-torque
switching. *Nature Materials, DOI: 10.1038/s41563-018-0137-y

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