



2024 Tokyo Tech Research Showcase

"Various Modalities in Al for Industry"

Venue: Rama Gardens Hotel / Online

Date: Friday, January 19, 2024

Time: 13:00 – 16:00 (THA), 15:00 - 18:00 (JST)

Free of charge / Advance registration required.

Tokyo Institute of Technology (Tokyo Tech), in cooperation with the National Science and Technology Development Agency (NSTDA), will host the **2024 Tokyo Tech Research Showcase** on Friday, January 19.

This event will cover research on "artificial intelligence (AI) technology for language, speech, and image processing". Generative AI is rapidly changing our lives, and how to effectively use AI is now an urgent issue for society. We invite you to join in this important discussion on effective AI utilization.

This event will be held in English.

Tokyo Tech Research Showcase is organized by Tokyo Tech ANNEX Bangkok,

co-sponsored by Tokyo Tech and NSTDA, and supported by JETRO Bangkok and Tokyo Tech Alumni Association Thailand (Thai Kuramae Kogyokai).

Registration

You can register to attend onsite or online:

Onsite: https://forms.gle/21ssBeqCVfDydk5BA



Online: https://us06web.zoom.us/webinar/
register/WN_mBSNP6qBQ4OU102n7Hk53g#/
registration



We will close onsite registration when venue capacity is reached, but you will still be able to attend online.

Please register by Wednesday, January 17, 2024.

Note: ① This workshop will be recorded. The recording will be later uploaded to the Tokyo Tech and NSTDA websites.

② Organizers may take photographs or videos during the event, which may be used on the Tokyo Tech and NSTDA websites.

Tokyo Tech ANNEX

To further advance academia-industry collaborations, Tokyo Tech established Tokyo Tech ANNEX Bangkok at Thailand Science Park in March 2018. Tokyo Tech ANNEX Bangkok facilitates the identification of potential seeds for international collaborative research with companies and other organizations in Thailand and the surrounding region.

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Tokyo Tech VR available You can get more information of the speakers at the main lobby.

Program

13:00 – 13:10	Opening Remarks Prof. Nobuhiro Hayashi Vice President for International Affairs, Tokyo Tech Dr. Patchralita Chatwalitpong Vice President, Science and Technology Human Resources Development, NSTDA
13:10 – 13:35	Assoc. Prof. Takahiro Shinozaki School of Engineering, Tokyo Tech "Spoken language acquisition by autonomous agents with intrinsic motivation"
13:35 – 14:00	Prof. Manabu Okumura Institute of Innovative Research Tokyo Tech "Language Processing and Its Applications in the Age of Pre-trained Models"
14:00 – 14:25	Dr. Prachya Boonkwan Language and Semantic Technology Research Team, AINRG, NECTEC "OpenThaiGPT: Large Language Models for Thai"
14:25 – 14:50	Prof. Koichi Shinoda School of Computing, Tokyo Tech "Advanced object detection techniques for visual scene understanding"
14:50 – 15:00	Closing Remarks
15:00 – 16:00	Coffee Break / Networking

Speakers' Profiles



Takahiro SHINOZAKI Associate Professor School of Engineering Tokyo Institute of Technology

Takahiro Shinozaki received the B.E., M.E., and Ph.D. degrees in computer science from the Tokyo Institute of Technology, Tokyo (Tokyo Tech), Japan, in 1999, 2001, and 2004, respectively. He is currently an Associate Professor at Tokyo Tech. From 2004 to 2006, he was a Research Scholar with the Department of Electrical Engineering, University of Washington, Seattle, WA, USA. His research interests include automatic spoken language acquisition and understanding, spoken language processing, anomalous sound detection, and their applications.



Manabu OKUMURA Professor Institute of Innovative Research Tokyo Institute of Technology

Manabu Okumura received B.E., M.E. and Dr. Eng. from Tokyo Institute of Technology (Tokyo Tech) in 1984, 1986 and 1989, respectively. He was an assistant professor at the Department of Computer Science, Tokyo Tech from 1989 to 1992, and an associate professor at the School of Information Science, Japan Advanced Institute of Science and Technology from 1992 to 2000. He was also a visiting associate professor at the Department of Computer Science, University of Toronto from 1997 to 1998. From 2000, he was an associate professor at Precision and Intelligence Laboratory, Tokyo Tech, and he is currently a professor at Institute of Innovative Research, Tokyo Tech. His current research interests include natural language processing, especially text summarization, computer assisted language learning, and text data mining.



Researcher
Language and Semantic Technology Research Team
Artificial Intelligence Research Group
National Electronics and Computer Technology
Center

Education:

2008 – 2013 PhD in Informatics at The University of Edinburgh, UK

Prachya Boonkwan

Experience:

2005 - NECTEC, Researcher

Research topics: Thai language processing, syntactic parsing, machine translation, language resource development, and machine learning. Projects at my responsibility include:

- ullet The development of a semi-supervised Thai parser scalable for large data
- The construction of Thai treebank based on contemporary Thai primary school readers Māna and Mānī



Koichi SHINODA
Professor
School of Computing
Tokyo Institute of Technology

Koichi Shinoda received the BS and MS degrees from the University of Tokyo, Tokyo, Japan in 1987 and 1989, respectively, both in physics, and the DEng degree in computer science from the Tokyo Institute of Technology (Tokyo Tech), Japan, in 2001. In 1989, he joined NEC Corporation. From 1997 to 1998, he was a visiting scholar with Bell Labs, Lucent Technologies, Murray Hill, NJ. From October 2001 to March 2003, he was an associate professor at the University of Tokyo, Japan. He is currently a professor at Tokyo Tech. His research interests include machine learning and multimedia information understanding.